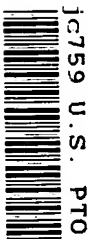


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
UTILITY PATENT APPLICATION TRANSMITTAL LETTER

FEE VALUE ACCOUNTABILITY	
DEPOSIT ACCOUNT NO.	
50	1086
FEE CODE	VALUE FURNISHED

1c821 U.S. PTO

10/072145



02/07/02

To: Assistant Commissioner for Patents
Box Patent Application
Washington D.C., 20231

Dear Sir:

Re Attorney Docket No. ONS00317, transmitted herewith is an application for Nonprovisional Utility Patent, entitled SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW SUBSTRATE CAPACITANCE, for filing under 37 C.F.R. §1.53(b), with a Mailing Date of February 7, 2002 and Express Mail Label No.: EK617876084US, as a New Patent Application.

The inventors are Guy E. Averett, Keith G. Kamekona, Chandrasekhara Sudhama, Weizhong Cai, Gordon L. Bratten, and Bladimiro Ruiz, Jr..

Patent application filing papers include,

- ✓ 15 page(s) of specification and claims, and 4 sheets of informal drawings.
- ✓ Newly executed oath or declaration combined with power of attorney on 1 page(s).
- ✓ An assignment transmittal letter and assignment of the invention to Semiconductor Components Industries, LLC.
- ✓ An information disclosure statement (IDS), with PTO-1449, and 5 prior art citations.
- ✓ Return receipt postcard.

The filing fee for claims as filed, adjusted by additions or deletions made to claims by amendment if any, is calculated as follows:

Total Claims	25 - 20 x \$18 =	\$90.00
Independent Claims	3 - 3 x \$80 =	\$0.00
Basic Fee	=	\$740.00
Total Filing Fee	=	\$830.00

Please charge Deposit Account No. 501086 in the amount of \$830.00 for the Total Filing Fee, and the Extension Fee under 37 C.F.R. §1.136(a) if applicable. The Commissioner is hereby authorized to charge any additional fees which may be required now or in the future during the entire pendency of this application under 37 C.F.R. §1.16 or 37 C.F.R. §1.17, including any present or future time extension fees which may be required, or credit any overpayment to Deposit Account No. 501086. This transmittal letter has 1 total page(s) and is submitted in triplicate. ✓

Feb. 7, 2002

Date
ON Semiconductor
Patent Administration Dept - MD A700
P.O. Box 62890
Phoenix, AZ 85082-2890

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